Bridge Amplifier for 2 x 1/4 Strain Gage Bridge
DABU AD2T-2Q

Features

- Industrial bridge amplifier for 2 x 1/4 S/G bridge
- For cyclical applications with reset function
- Voltage output
- Protection class IP 65

Electrical Data

Output signal		±10 V calibrated (max. ±12 V)
Characteristic curve deviation		< 0,2%
Supply voltage range		18 - 33 VDC
Current draw		< 60 mA < 40 mA @ 24 VDC
Bridge excitation		approx. 9 VDC
S/G bridge resistance		350 Ω (R_C)
Output impedance		22 Ω
Tare accuracy		0250 < 15 mV 0350 < 12 mV 0500 < 7 mV 1000 < 5 mV
Reset input	active inactive	5 - 33 VDC < 2 mA < 1 VDC
Tare range		±6 mV/V
Reset puls		> 1 ms
Reset settle time		< 5 ms
Frequency range (-3 dB)		1'000 Hz
Signal polarity		Bipolar
Noise		(0 ... 5 kHz) 0250 < 15 mV_{pp} 0350 < 12 mV_{pp} 0500 < 7,5 mV_{pp} 1000 < 5 mV_{pp}

Mechanical Data

Control connection	5 pin male (Series 713)
Sensor connection	4 pin female (Series 712)
Enclosure	aluminum anodised

Environmental Conditions

Operating temp. range	-25...+85 °C
Specified temp. range	0...+70 °C
Storage temperature	-40...+100 °C
Protection class	IP 65

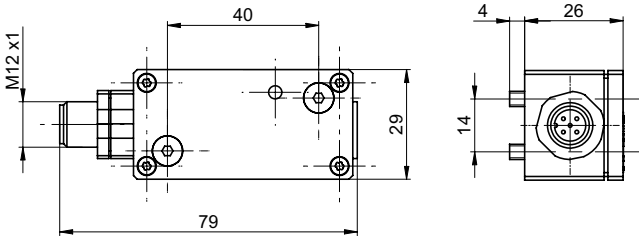
Order Code

DABU AD2T-2Q ☐☐☐☐

Gain

0250	0250 με = 0 - 10 V
0350	0350 με = 0 - 10 V
0500	0500 με = 0 - 10 V
1000	1000 με = 0 - 10 V

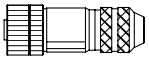
Dimensions (mm)



Delivery Contents

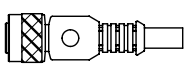
- Mounting screw 2 pcs. M4 x 30

Accessories (not included in delivery)



Series 713

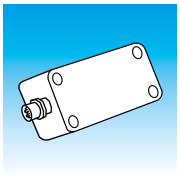
Connector female, control side, 5-pin, Part No. 10135462



M12 x 1

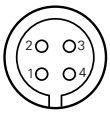
Connector female with cable, control side, 5-pin

ESG 34CH0200G 5-pin (shielded) 2 m, PUR, (Part No. 11046264)
ESG 34CH0500G 5-pin (shielded) 5 m, PUR, (Part No. 11046266)
ESG 34CH1000G 5-pin (shielded) 10 m, PUR, (Part No. 10155587)



Electrical Connection

Sensor side Series 712

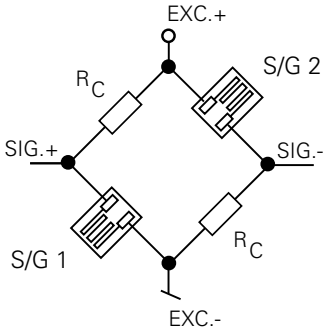


Pin	Signal	
1	DMS 1	EXC.+
2	DMS 1	SIG.-
3	DMS 2	SIG.+
4	DMS 2	EXC.-

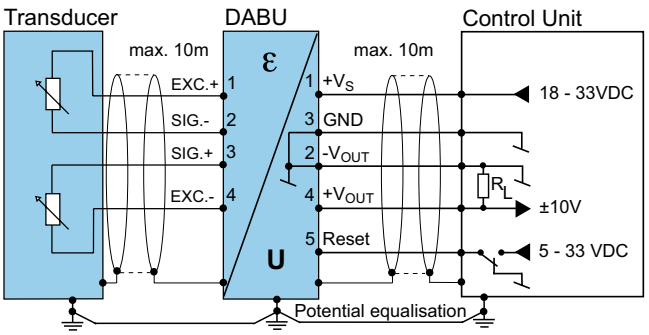
Control side Series 713

Pin number	Signal
1	+Vs
2	-V_{OUT}
3	GND
4	+V_{OUT}
5	Reset

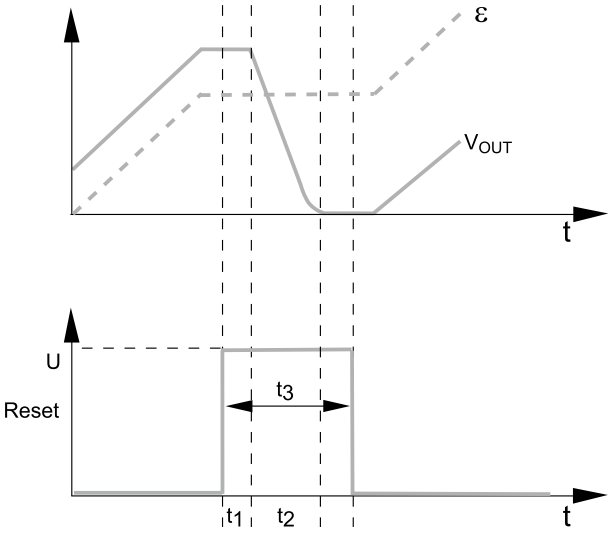
S/G Bridge



Control



Reset Function



V_{OUT}	Output signal
ε	Input signal
Reset	Reset input (active high)
t_1	Reset delay (< 0,3 ms)
t_2	Reset time (< 5 ms)
t_3	Reset impuls (> 1 ms)